

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L6	11	"4841168"	US-PGPUB; USPAT; USOCR; EPO; JPO	ADJ	ON	2008/12/17 19:14
L9	103	(latch and differential and sixth transistor).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO	ADJ	ON	2008/12/17 19:59
L10	96	(second latch and differential and (opposite or cross coupled)).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO	ADJ	ON	2008/12/17 20:02
S1	3	("4841168") or ("3339089").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2008/06/09 11:49
S2	1631	327/115,117,218.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO	ADJ	ON	2008/06/09 14:13
S3	237	327/115,117,218.ccls. and differential	US-PGPUB; USPAT; USOCR; EPO; JPO	ADJ	ON	2008/06/09 14:14
S4	1	("4675553").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2008/06/09 14:30
S5	53	327/115,117,218.ccls. and differential and threshold	US-PGPUB; USPAT; USOCR; EPO; JPO	ADJ	ON	2008/06/09 14:53
S6	1637	(inverting near3 output) WITH (noninverting near3 input)	US-PGPUB; USPAT; USOCR; EPO; JPO	ADJ	ON	2008/06/09 17:45
S7	307	(inverting near3 output) WITH (noninverting near3 input) and "327".clas.	US-PGPUB; USPAT; USOCR; EPO; JPO	ADJ	ON	2008/06/09 17:46
S8	2	("3339089").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2008/06/12 14:28

S9	596	((second latch) or (second near2 flop)) and common mode	US-PGPUB; USPAT; USOCR; EPO; JPO	ADJ	ON	2008/12/09 17:27
S10	478	((second latch) or (second near2 flop)) and common mode and differential	US-PGPUB; USPAT; USOCR; EPO; JPO	ADJ	ON	2008/12/10 09:15
S11	93	327/199-212,218.ccls. and common mode and differential	US-PGPUB; USPAT; USOCR; EPO; JPO	ADJ	ON	2008/12/10 09:56
S12	76	differential flip near2 flop	US-PGPUB; USPAT; USOCR; EPO; JPO	ADJ	ON	2008/12/10 11:07
S13	4	("4841168") or ("3339089") or ("20030001646").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2008/12/10 11:32
S14	26644	differential and flip near2 flop	US-PGPUB; USPAT; USOCR; EPO; JPO	ADJ	ON	2008/12/10 11:50
S15	611	differential.ti. and flip near2 flop	US-PGPUB; USPAT; USOCR; EPO; JPO	ADJ	ON	2008/12/10 11:51
S16	895	327/199-212,218.ccls. and differential	US-PGPUB; USPAT; USOCR; EPO; JPO	ADJ	ON	2008/12/10 13:00
S17	147	327/199-212,218.ccls. and frequency divider	US-PGPUB; USPAT; USOCR; EPO; JPO	ADJ	ON	2008/12/16 13:03
S18	521	327/115,117.ccls. and ((latch or (flip near2 flop)) and clock)	US-PGPUB; USPAT; USOCR; EPO; JPO	ADJ	ON	2008/12/16 13:35
S19	393	377/47,48.ccls. and ((latch or (flip near2 flop)) with clock)	US-PGPUB; USPAT; USOCR; EPO; JPO	ADJ	ON	2008/12/16 14:55
S20	495	377/47,48.ccls. and ((latch or (flip near2 flop)) and clock)	US-PGPUB; USPAT; USOCR; EPO; JPO	ADJ	ON	2008/12/16 14:55

S21	102	327/199-212,218.ccls. and common mode	US-PGPUB; USPAT; USOCR; EPO; JPO	ADJ	ON	2008/12/17 14:46
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